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<b>Complete If Known</b>			
Substitute for form 1449A/PTO		Application Number	52352147
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	January 18, 2005
		First Named Inventor	Yasushi SHIMADA et al.
		Art Unit	2818
		Examiner Name	PHUOC DANG
If no or many sheets are necessary			
Sheet	1	of	2
		Attorney Docket Number	1204-44657X00

U.S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
PD		09-148746	06/06/1997	JAPAN	—	ABST
		10-013036	01/16/1998	JAPAN	—	ABST
		2001-068858	03/16/2001	JAPAN	—	ABST
		01-189999	07/31/1989	JAPAN	—	ABST
		07-183665	07/21/1995	JAPAN	—	ABST
		06-085462	03/25/1994	JAPAN	—	ABST
PD		05-055079	03/05/1993	JAPAN	—	ABST

Examiner Signature	PATRICK DANG	Date Considered	9/26/2006
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DT15 R- PCT/PTO 1.8 JAN 2005

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Substitute for form 1449/PTO					
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<i>Complete if Known</i>	
				Application Number	10/221470
				Filing Date	January 18, 2005
				First Named Inventor	Yasushi SHIMADA et al.
				Group Art Unit	2818
				Examiner Name	PHUOC DANG
Sheet	2	of	2	Attorney Docket Number	1204.44657X00

NON-PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
PD		Electrical Properties of Buried Capacitor In Zero X-Y Shrinkage Multilayer Ceramic Substrate, Osama Inoue; Seiichi NAKATANI; Junichi KATO; and Yutaka TAGUCHI vol. 4, No. 2(2001) pages 145-149			
		Examination of the Built-In STO Film Capacitor Process Technology on Resin-Molded High Frequency MCM and the Capacitor Frequency Characteristics, Hiroji YAMADA and Kichii YAMASHITA col. 4 No.7 (2001) page 590-596			
		Integration of Thin Film Passive Circuits Using High/Low Dielectric Constant Materials, P. Chahal; A. Haridas; A. Pham; R.R. Tummala; M.G. Allen; m. Swaminathan and J. Laskar, 1997 Electronic Components and Technology Conference pages 739-744			
PD		National Center for Manufacturing Sciences Pages 3-1-3-6			

Examiner Signature	PHUOC DANG	Date Considered	9/26/2006
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<p><b>Substitute for form 1449A/PTO</b></p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p><i>(use as many sheets as necessary)</i></p> <p>April 18, 2005</p>				<b>Complete if Known</b>	
				Application Number	10/521,470
				Filing Date	01/18/2005
				First Named Inventor	Y. SHIMADA
				Art Unit	2812 2818
				Examiner Name	Unassigned P HUC DANG
Sheet	1	of	2	Attorney Docket Number	1204.44657X00

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Examiner Signature	PHUU <sup>L</sup> DANG	Date Considered	9/26/2006
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				Filing Date	01/18/2005
				First Named Inventor	Y. SHIMADA, et al.
				Group Art Unit	2818
				Examiner Name	Unassigned PI TUC DANG
Sheet	2	of	2	Attorney Docket Number	1204.44657X00
(use as many sheets as necessary)					

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P D		S. K. Bhattacharya, et al., "Next generation integral passives: materials, processes, and integration of resistors and capacitors on PWB substrates", <i>Journal of Materials Science: Materials in Electronics</i> , Vol. 11, pp. 253-268, 2000.	

Examiner Signature	PHUZ DANG	Date Considered	9/26/2006
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Institute for form 1449A/PTOINFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

June 14, 2006

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Sheet

1

of

2

Attorney Docket Number

## Complete if Known

Application Number	10/521,470
Filing Date	01/18/2005
First Named Inventor	Y. SHIMADA
Art Unit	2818
Examiner Name	Andy Huhm PHUC DANG

1204,44657X00

## U.S. PATENT DOCUMENTS

Examiner Initials'	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-			

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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
PD		JP 2002-043714	02/08/2002	T. Hirakawa	—	ABST
A		JP 2002-009416	01/11/2002	N. Fukuya, et al.	—	ABST
		JP 06-232005	08/19/1994	K. Hawashi	—	ABST
		JP 05-283864	10/29/1993	R. Kondo, et al.	—	ABST
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		JP 2000-133900	05/12/2000	K. Kobayashi, et al.	—	ABST
		JP 2000-030534	01/28/2000	S. Iwasa, et al.	—	ABST
		JP 11-0177244	07/02/1999	K. Yoneyama, et al.	—	ABST
		JP 53-149673	12/27/1978	—	—	ABST
		JP 2000-269065	09/29/2000	A. Inuzuka, et al.	—	ABST
		JP 2002-097441	04/02/2002	S. Katogi, et al.	—	ABST
		JP 06-164150	06/10/1994	K. Tani, et al.	—	ABST
		JP 60-089912	05/20/1985	—	—	ABST
		JP 10-098245	04/14/1998	T. Takada, et al.	—	ABST
		JP 09-083147	03/28/1997	N. Sugimoto, et al.	—	ABST
PD		JP 2002-185104	06/28/2002	K. Tsuyama, et al.	—	ABST

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## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

June 14 2006

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<b>TRADEMA</b> <small>Institute for form 1449/PTO</small>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <b>June 14, 2006</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
Application Number		10/521,470			
Filing Date		01/18/2005			
First Named Inventor		Y. SHIMADA, et al.			
Group Art Unit		2818			
Examiner Name		Andy Huynh PHUC DANG			
Sheet	2	of	2	Attorney Docket Number	
				1204.44657X00	

## NON-PATENT LITERATURE DOCUMENTS

Examiner Signature	RHVC DANG	Date Considered	9/26/2006
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Sheet 1 of 2

Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Application Number</b>	10/521,470
				<b>Filing Date</b>	1/18/2005
				<b>First Named Inventor</b>	Yasushi SHIMADA, et al.
				<b>Art Unit</b>	2818
				<b>Examiner Name</b>	Phuc T. DANG
<i>(Use as many sheets as necessary)</i>				<b>Attorney Docket Number</b>	1204 44657X00
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
P D		2000-133907	05-12-00	Kyocera Corp.	—	A§
P D		07-022725	01-24-95	Shinko Electric Ind. Co. Ltd.	—	A§

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				Application Number	10/521,470
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				First Named Inventor	Yasushi SHIMADA
				Group Art Unit	2818
				Examiner Name	Phuc T. DANG
Sheet	2	of	2	Attorney Docket Number	44657X00

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Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
P D		Japanese Official Action dated June 27, 2006, for Japanese Application No. 2002-259291	

Examiner Signature	PHUOC DANG	Date Considered	9/26/2006
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		Filing Date	01/18/2005	
		First Named Inventor	Y. SHIMADA	
		Art Unit	2818	
		Examiner Name	Phuc T. Dang	
Sheet	1	of	1	Attorney Docket Number

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P D		JP 2002-016366	01/18/2002	T. Matsunaga
P D		JP 2000-277922	10/06/2000	Y. Inui, et al.
P D		JP 2002-280261	09/27/2002	T. Namatame, et al.

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P D		Japanese Official Action, for Application No. 2002-324238, dated July 25, 2006.		

Examiner Signature	P. HUc T. DANG	Date Considered	9/26/2006
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